

PATENT  
0465-0881P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ji Soo PARK et al. Conf:

Serial No.: NEW CONTINUATION APPLICATION Group: 2825  
BASED ON SERIAL NO. 09/377,495

Filed: December 3, 2001 Examiner: B. Keshavan

For: METHOD FOR FORMING GATE IN SEMICONDUCTOR DEVICE

**PRELIMINARY RESPONSE**

Assistant Commissioner for Patents Date: December 3, 2001  
Washington, D.C. 20231

Dear Sir:

The following remarks are submitted in connection with the above-identified application.

**REMARKS**

Claims 1-21 are pending. Claims 1 and 10 are independent claims. These remarks correspond to those submitted on November 1, 2001 in the parent application (U.S. Serial No. 09/377,495).

**Rejections Under 35 U.S.C. §103**

Claims 1-10 and 12-21 (as numbered in the parent application) were rejected under 35 U.S.C. §103(a) as being unpatentable over Doyle et al. (U.S. Patent No. 6,025,254), and in view of Broadbent et al. (IEEE Transactions and